

Towards Lean 4: An Optimized Object Model for an Interactive Theorem Prover

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www.kit.edu

The Lean theorem prover



- dependently-typed proof assistant
- small trusted kernel
- also a purely functional, eager programming language

```
inductive list (α : Type u)
| nil : list
| cons : α → list → list
```

```
def map (f : \alpha \rightarrow \beta) : list \alpha \rightarrow list \beta

| [] := []

| (x :: xs') := f x :: map xs'
```

https://leanprover.github.io

A brief history of Lean



- Lean 0.1 (2014)
- Lean 2 (2015)
 - first official release
 - fixed tactic language
- Lean 3 (2017)
 - make Lean a meta-programming language: build tactics in Lean
 - backed by a bytecode interpreter
- Lean 4 (201X)
 - make Lean a general-purpose language: native back end, FFI, ...
 - reimplement Lean in Lean

Lean 3 backend





Lean 4 backend





Lean 3 object model



Uniform model: every value is a tagged pointer representing one of

- a 31-bit number
- a reference to a ref-counted VM object
 - a constructor value
 - a closure
 - an arbitrary-precision integer
 - any C++ object derived from vm_external

Lean 3 constructor object



- 4 bytes reference counter
- 1 byte object kind
- 4 bytes constructor index
- 4 bytes #fields
- 4/8 bytes field #0

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- Eventually needed to move objects between threads ⇒ fall back to deep-copying...
- Every object is a C++ smart pointer ⇒ simple to use, but no way to optimize RC ops
- Core types like name and expr are not VM objects ⇒ need to be wrapped in vm_external for every operation

Lean 4 object model



Non-uniform model: in the lowest IR, each value has one of the types

- int8/uint8/.../uint64: unboxed primitive value
- _obj: tagged pointer to a VM object
 - a constructor, closure, or bigint
 - an array of boxed or unboxed values
 - a thunk

Lean 4 constructor object



 $1 \ \mbox{byte}$ object kind

- 1 byte memory kind
- 2 bytes constructor index
- 2 bytes **#boxed** fields
- 2 bytes #unboxed bytes
- 4/8 bytes boxed field #0

... .

....

X bytes unboxed field #0

All boxed fields come first \implies free can still be implemented uniformly

Memory kind



- single-threaded: non-atomic RC
 - the default for heap allocations
- multi-threaded: atomic RC
 - threading primitives upgrade object graphs crossing threads to this kind
 - everything is immutable \Longrightarrow ST object never reachable from MT object
- stack: no RC
- region: no RC



writing a good GC is really hard

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- easier to use from other languages
- everything is immutable ⇒ no cycles!
- explicit ref count \implies can do destructive updates on RC = 1
 - like linear types, but checked dynamically
 - dependent types are hard enough
 - more precise (but also less predictable)

Dynamic linearity



def map $(f : \alpha \rightarrow \beta)$: list $\alpha \rightarrow$ list β | [] := [] | (x :: xs') := f x :: map xs'

Dynamic linearity



```
def map (f : \alpha \rightarrow \beta) : list \alpha \rightarrow list \beta

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```

```
[compiler.llnf]

A (f xs : _obj),

list.cases_on xs

(let _x_1 : _obj := _dec f

in _cnstr.0)

(let _x_1 : _obj := _proj.0 xs,

_x_2 : _obj := _inc _x_1,

_x_3 : _obj := _proj.1 xs,

_x_4 : _obj := _inc _x_3,

_x_5 : _obj := _reset.2 xs,

_x_6 : _obj := _apply f _x_2,

_x_7 : _obj := list.map f _x_4

in _reuse.1 _x_5 _x_6 _x_7)
```

_reset / _reuse check for linearity at runtime \implies unique prefix of a list will be reused even if remainder is shared!

Dynamic linearity



Benchmarks of direct C++ implementations of

list.map (+1) (list.range 4000)

optimizations	run time of map
no reuse	214.3 µs
_reset / _reuse	27.7 μs
optimized reuse	12.3 µs
known unique	10.7 μs

Borrowing



```
def length : @borrowed (list α) → nat
| [] := 0
| (x :: xs') := length xs' + 1
```

The Oborrowed attribute

- delays/avoids RC operations:
 - no inc/dec when passing an argument to a borrow parameter
 - inc when returning/passing a borrowed value to a non-borrow parameter
- but prevents linear updates



Lean 3 startup does not scale well: deserializing all dependencies can take significant time and memory



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- lazy loading and prefetching provided by the OS
 - proofs aren't needed usually



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What if we could just mmap (multiple!) regions of objects into memory?

- lazy loading and prefetching provided by the OS
 - proofs aren't needed usually
- everything immutable
 - \implies pages can even be shared by multiple Lean processes
 - careful: must not touch RC



We're investigating two approaches:

Simple approach: use relative pointers in region objects

introduces branch for retrieving unboxed field



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Advanced approach: try to mmap each region to its original address

- on collision: fall back to eager loading and pointer patching
- \blacksquare probability of a single collision between 100 dependencies of size 10 MB in 48-bit address space is ${\sim}0.018\%$



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In either approach, writing objects to disk does need some transformations



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Unboxed fields now make it feasible to reimplement core types as Lean objects!

```
expr mk_const(name const & n, levels const & ls) {
    expr r(mk_cnstr(static_cast<unsigned>(expr_kind::Const), n, ls, expr_scalar_size(expr_kind::Const)));
    set_scalar<expr_kind::Const>(r, hash(n.hash(), hash(ls)), false, has_mvar(ls), false, has_param(ls));
    return r;
```

}



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```

}

Unboxed metadata is at the end of the object \implies can be hidden in the Lean definition

```
inductive expr
| const : name → list level → expr
| ...
```

Implementation status



- object model runtime in C++
- core types ported to model
- optimizing compiler from Core Lean to LLNF
 - inlining, specialization, simplification
 - using join-point representation
- compiler from LLNF to old bytecode format
- model used by backends and built-ins
- writing and loading regions
- multi-threading
- borrowing

Conclusion



- A new object model customized to the needs of a theorem prover
- utilizing properties of an eager, purely functional language
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Thank you!